TR-3: THE OPEN ARCHITECTURE BACKBONE OF THE F-35 LIGHTNING II

Technology Refresh-3 is the enabler for unlocking the full capability of Block 4 modernization on the F-35 – keeping the warfighter ahead of emerging threats

Integrated Core Processor

> Multi-function processor for processing data for the aircrafts radar, distributed aperture system, electronic warfare, communications, guidance and control, cockpit and helmet displays

> 37-fold increase in computing power for planned capability upgrades

> The ICP system provides enhanced software stability, higher reliability and increased diagnostics resulting in decreased sustainment costs

Panoramic Cockpit Display - Electronic Unit

> Legacy capabilities increase five-fold with modern multi-core processors

> Two independent safety critical display processors for left and right cockpit displays with redundancy

Aircraft Memory System

> Delivers twenty times the storage of the legacy system enabling increased sensor data gathering capabilities to expand situational awareness

INTEGRATED CORE PROCESSOR

PANORAMIC COCKPIT DISPLAY ELECTRONIC UNIT

AIRCRAFT MEMORY SYSTEM

STRENGTHENING OUR NATION'S AIRPOWER

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